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FAX: 1 (703) 872-9306
DATE: 06-22-2005 TIME: 4:15 PM
☐ WITH CONFIRMATION
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RE: APPLICATION NO. 10/733,884
FILED: 12-03-2003

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By: Jennie Heston
Jennie Heston

SUBMITTED HERewith FOR THE ABOVE-CAPTIONED APPLICATION:

- Transmittal Form
- Amendment and Request for Reconsideration

Respectfully submitted,

Andrew D. Fortney
Andrew D. Fortney, Ph.D.; Reg. No. 34,600

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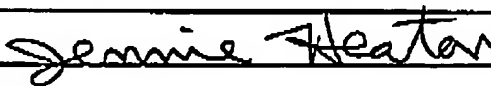
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TRANSMITTAL FORM	Application Number	10733,884	
	Filing Date	12-03-2003	
	First Named Inventor	Jae Suk LEE et al.	
	Art Unit	2823	
	Examiner Name	DANG, Trung	
(to be used for all correspondence after initial filing)			
Total Number of Pages in This Submission	14	Attorney Docket Number	OF03P212/US

ENCLOSURES (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input checked="" type="checkbox"/> Amendment/Reply <input checked="" type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Reply to Missing Parts/ Incomplete Application <input type="checkbox"/> Reply to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____ <input type="checkbox"/> Landscape Table on CD	<input type="checkbox"/> After Allowance Communication to TC <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input type="checkbox"/> Other Enclosure(s) (please identify below): 1) Fax Transmission Cover Sheet
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Firm Name	The Law Offices of Andrew D. Fortney, Ph.D., P.C.		
Signature			
Printed name	Andrew D. Fortney, Ph.D.		
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I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:			
Signature			
Typed or printed name	Jennie Henton	Date	06-22-2005

This collection of information is required by 37 CFR 1.6. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Atty. Docket No. OF03P212/US

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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

:

Jae Suk LEE et al.

: GROUP ART UNIT: 2823

APPLICATION NO: 10/733,884

:

FILED: DECEMBER 3, 2003

: EXAMINER: DANG, Trung

FOR: METHOD OF MANUFACTURING
A SEMICONDUCTOR DEVICE

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By:


Jennie HeatonAMENDMENT AND REQUEST FOR RECONSIDERATION UNDER 35 U.S.C. 132
AND 37 C.F.R. 1.111

Mail Stop AF
COMMISSIONER FOR PATENTS
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ALEXANDRIA, VA 22313-1450

SIR:

Responsive to the Office Action dated March 25, 2005, Applicant elects, with traverse, Group I, Claims 1 and 3-9, drawn to a method of manufacturing a semiconductor device, and (also with traverse) Group A, Claims 1 and 3-9, directed to a method that comprises etching parts of the insulating layer where a plurality of via holes will be formed to a certain thickness so as not to expose the first conductive line, and respectfully requests (i) entry of the following amendments and (ii) consideration of the following remarks.